Tiger Lake Platform RVP EC Release Notes

WW12, 2020

Intel Confidential

By using this document, in addition to any agreements you have with Intel, you accept the terms set forth below.

You may not use or facilitate the use of this document in connection with any infringement or other legal analysis concerning Intel products described herein. You agree to grant Intel a non-exclusive, royalty-free lTigernse to any patent claim thereafter drafted which includes subject matter disclosed herein.

INFORMATION IN THIS DOCUMENT IS PROVIDED IN CONNECTION WITH INTEL PRODUCTS. NO LTIGERNSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. EXCEPT AS PROVIDED IN INTEL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, INTEL ASSUMES NO LIABILITY WHATSOEVER AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO SALE AND/OR USE OF INTEL PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

A "Mission Critical Application" is any application in which failure of the Intel Product could result, directly or indirectly, in personal injury or death. SHOULD YOU PURCHASE OR USE INTEL'S PRODUCTS FOR ANY SUCH MISSION CRITICAL APPLICATION, YOU SHALL INDEMNIFY AND HOLD INTEL AND ITS SUBSIDIARIES, SUBCONTRACTORS AND AFFILIATES, AND THE DIRECTORS, OFFTIGERRS, AND EMPLOYEES OF EACH, HARMLESS AGAINST ALL CLAIMS COSTS, DAMAGES, AND EXPENSES AND REASONABLE ATTORNEYS' FEES ARISING OUT OF, DIRECTLY OR INDIRECTLY, ANY CLAIM OF PRODUCT LIABILITY, PERSONAL INJURY, OR DEATH ARISING IN ANY WAY OUT OF SUCH MISSION CRITICAL APPLICATION, WHETHER OR NOT INTEL OR ITS SUBCONTRACTOR WAS NEGLIGENT IN THE DESIGN, MANUFACTURE, OR WARNING OF THE INTEL PRODUCT OR ANY OF ITS PARTS.

Intel may make changes to specifications and product descriptions at any time, without notTiger. Designers must not rely on the absence or characteristics of any features or instructions marked "reserved" or "undefined". Intel reserves these for future definition and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to them. The information here is subject to change without notTiger. Do not finalize a design with this information.

The products described in this document may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Contact your local Intel sales offTiger or your distributor to obtain the latest specifications and before placing your product order.

Copies of documents which have an order number and are referenced in this document, or other Intel literature, may be obtained by calling 1-800-548-4725, or go to: http://www.intel.com/design/literature.htm

Intel® Rapid Start Technology requires a select Intel® processor, Intel® software and BIOS update, and a Solid-State Drive (SSD) or hybrid drive. Depending on system configuration, your results may vary. Contact your system manufacturer for more information

Intel and the Intel logo are trademarks or registered trademarks of Intel Corporation or its subsidiaries in the United States and other countries.

*Other names and brands may be claimed as the property of others.

Intel Confidential

Contents

Revision History

1.1 TIGER Lake RVP

Revision Number	Description	Revision Date
EC 1.6	Initial Revision - Please see release notes in the package	WW28'19
EC 1.09	Please see release notes in the package	WW33'19
EC 1.10	Please see release notes in the package	WW34'19
EC 1.11	Please see release notes in the package	WW36'19
EC 1.12	Please see release notes in the package	WW43'19
EC 1.13	Please see release notes in the package	WW44'19
EC 1.14	Please see release notes in the package	WW46'19
EC 1.15	Please see release notes in the package	WW46'19
EC 1.16	Please see release notes in the package	WW02'20
EC 1.17	Please see release notes in the package	WW04'20
EC 1.18	Please see release notes in the package	WW06'20
EC 1.19	Please see release notes in the package	WW10'20
EC 1.21	Please see release notes in the package	WW12'20

1.2   Files Included in this kit

TGL_EC-01.21_3_10_2020.ZIP

Note: EcRegionPointer.bin is included in the release with a pointer to a fixed EC region starting at 0x1000 in the IFWI to help with IFWI stiching in the field for eSPI-MAF configuration.

1.3   Resolved KSC issues

Please refer to RELNOTES.txt included in each .zip file for details.

1.4   Known Issues / Limitations

n/a