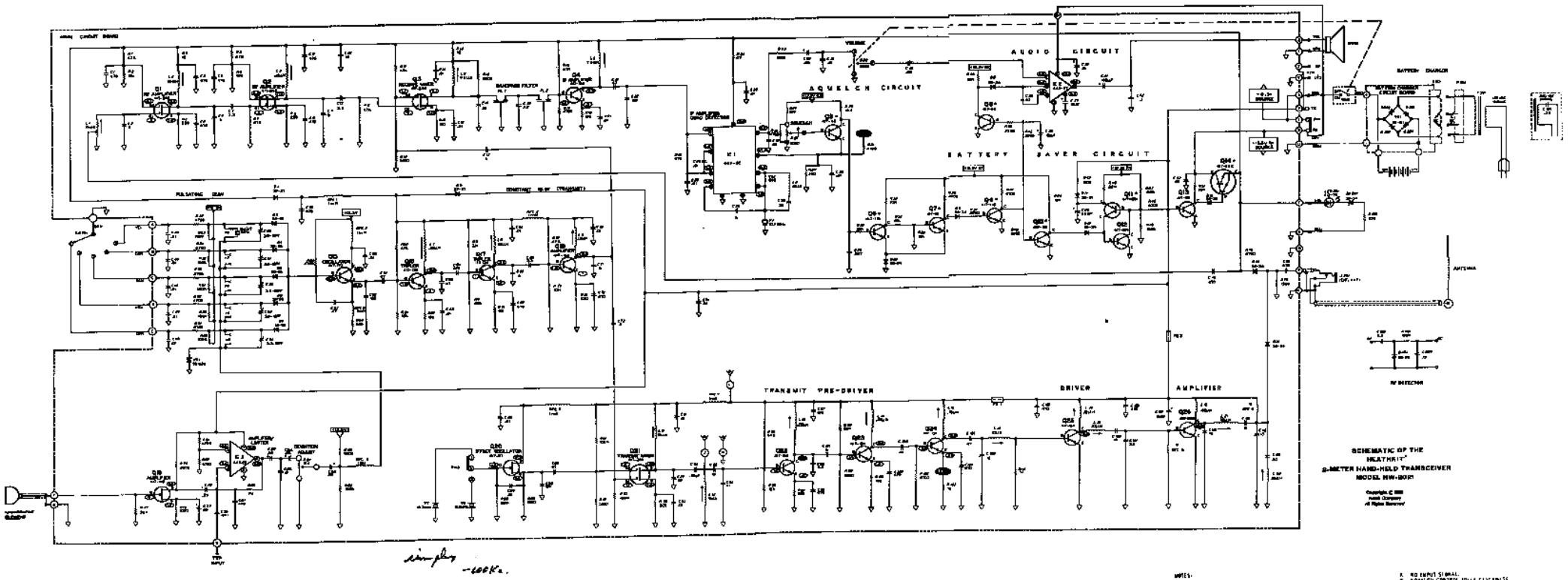

simply -LOOK.

TABLE 1 SQUELCH CONTROL FULLY CLOCKWISE. NO INPUT SIGNAL	E	B	C
Q5	.4	1.2	22
Q6	.6	1.1	6.9
Q7	.6	1.1	0.9
Q8	0	.7	.3
Q9	0	.1	3.6
Q10	0	.7	.1
Q11	–	0.4	–

TABLE 2 SQUELCH CONTROL FULLY COUNTER CLOCKWISE. NO INPUT SIGNAL	E	B	C
Q5	.5	0	2.4
Q6	.6	.4	.6
Q7	.4	.7	5.9
Q8	0	.1	22.5
Q9	0	.7	.1
Q10	0	.1	3.4
Q11	17.5	17.5	11.6

NOTES:

1. COMPONENT NUMBERS ARE IN THE FOLLOWING GROUPS
   1–199 PARTS ON THE MAIN CIRCUIT BOARD
   201–299 PARTS ON THE CASE
   301–399 PARTS IN THE BATTERY CHARGER.
   401–499 PARTS ON THE RF DETECTOR.
2. ALL RESISTORS ARE 5% UNLESS 1% TOLERANCE, UNLESS DIGIKEYISI NOTED. RESISTOR VALUES ARE IN OHMS K=1000 M=1,000,000
3. CAPACITORS EQUAL TO OR LESS THAN 1.0 ARE IN µF (MICROFARADS). ALL OTHER CAPACITORS ARE IN µµF (PICOFARADS) UNLESS OTHERWISE MARKED
4. INDUCTORS ARE SHOWN IN MH (MILLIHENRIES) AND UH (MICROHENRIES)
5. THIS SYMBOL INDICATES A DC VOLTAGE MEASUREMENT TAKEN WITH A HIGH INPUT IMPEDANCE VOLTMETER FROM THE POINT INDICATED TO CHASSIS GROUND UNDER THE FOLLOWING CONDITIONS
   A. NO INPUT SIGNAL.
   B. SQUELCH CONTROL FULLY CLOCKWISE.
   C. VOLUME CONTROL FULLY CLOCKWISE.
   D. LOWEST FREQUENCY OSCILLATOR CRYSTAL IN USE.
   E. TRANSMITTER VOLTAGES. TESTS WITHOUT MODULATION
   a. THIS SYMBOL INDICATES CHASSIS GROUND.
   b. THIS SYMBOL INDICATES A SOLDERED CONNECTION ON THE MAIN CIRCUIT BOARD.
   c. SEE TABLES 1 AND 2 FOR VOLTAGES.
   d. THIS SYMBOL INDICATES A [illegible] BUILDER.
6. REFER TO THE "CIRCUIT BOARD [illegible]" FOR AREAS [illegible] LOCATION OF PARTS.
7. INDICATES TEST POINT
8. INDICATES TEST POINT USED ONLY WHEN ALIGNMENT PERFORMED WITHOUT [illegible]